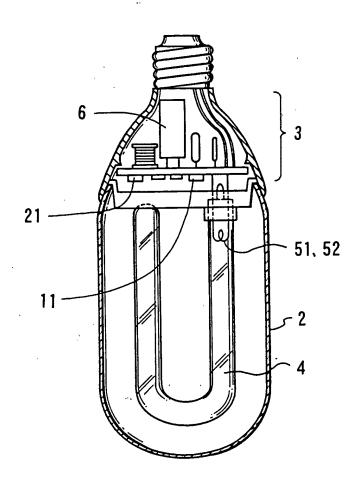


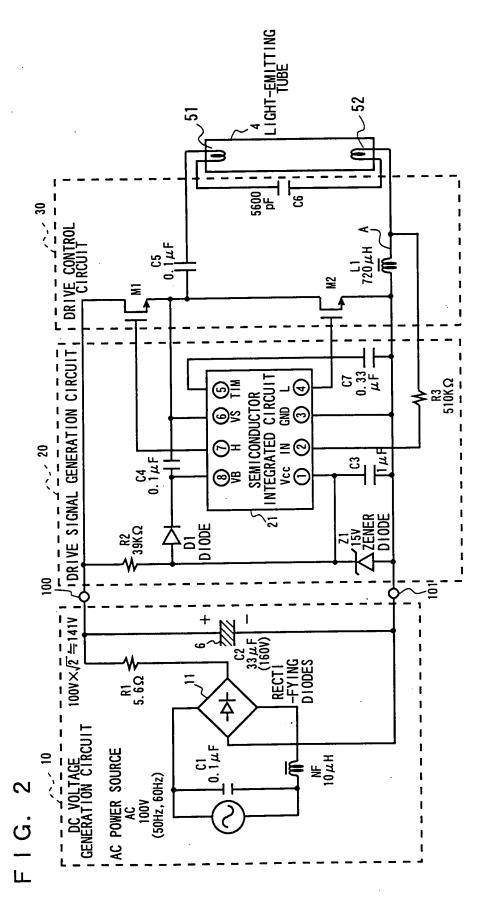
FIG. 1



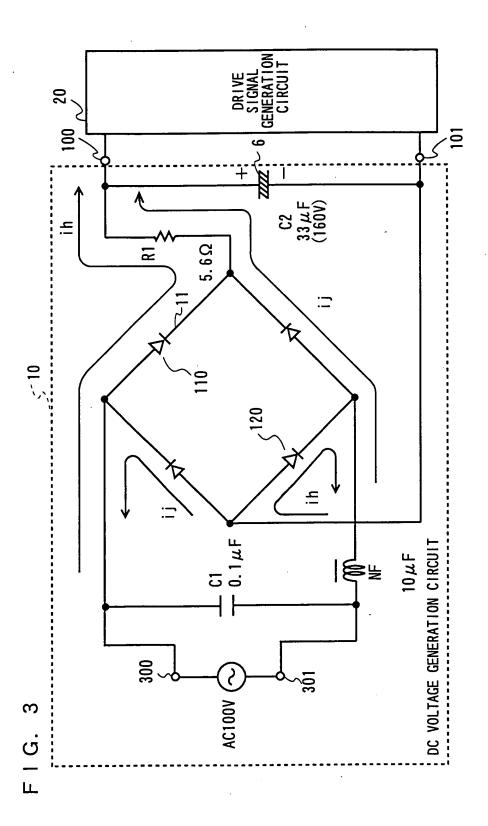
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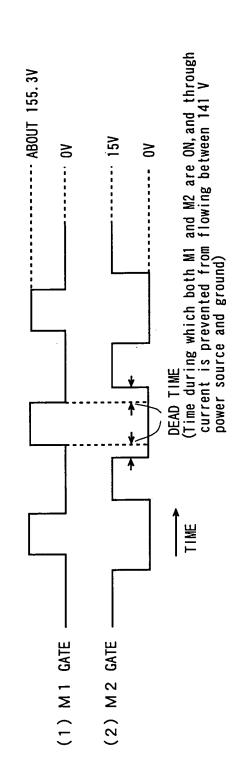
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ABOUT 141 V DC POINT 303 POINT 302 (c) VOLTAGE BETWEEN 100 AND 101 (WITH C2) (a) 100 V AC, VOLTAGE BETWEEN 300 AND 301 (b) VOLTAGE BETWEEN 100 AND 101 (WITHOUT C2)

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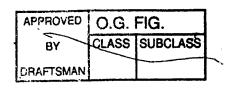
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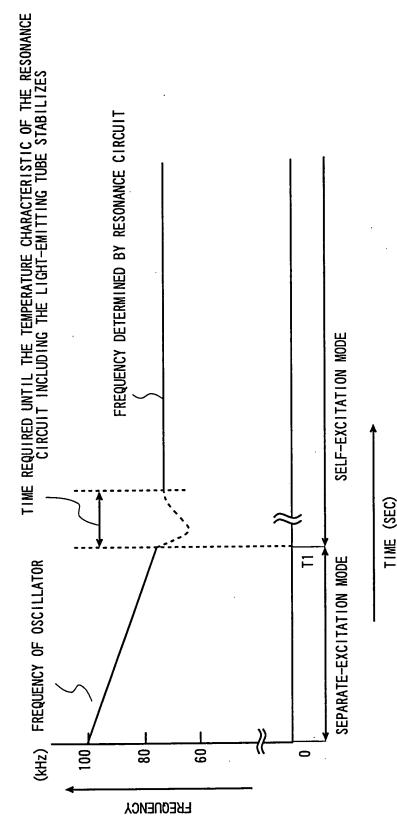


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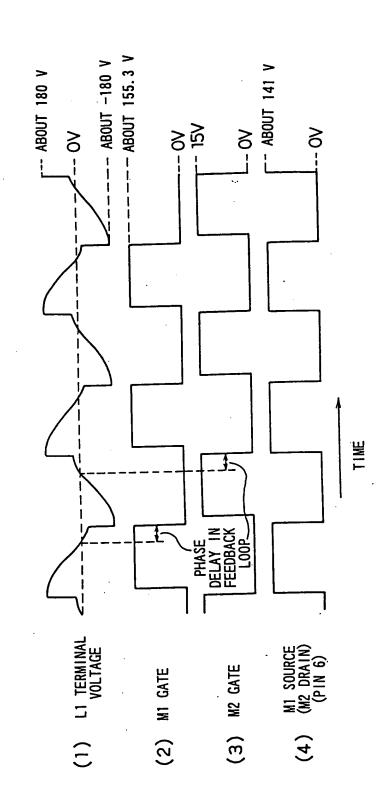
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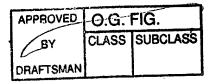


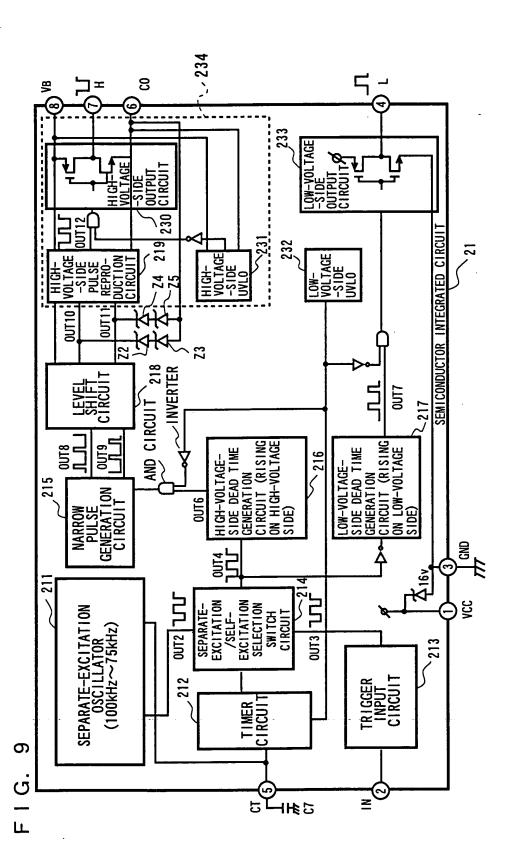
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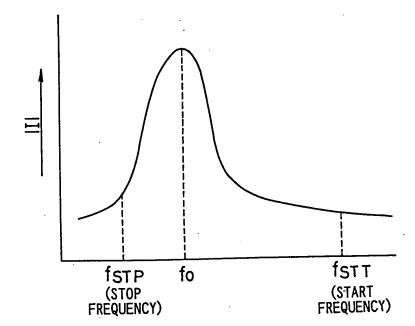
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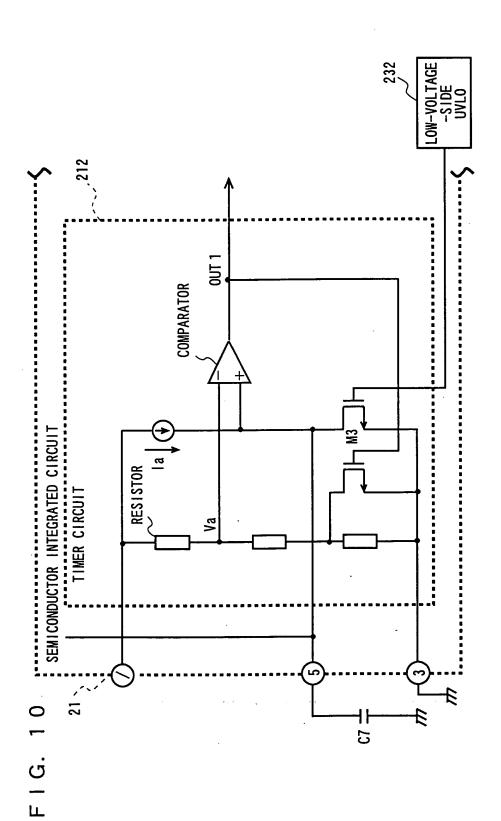




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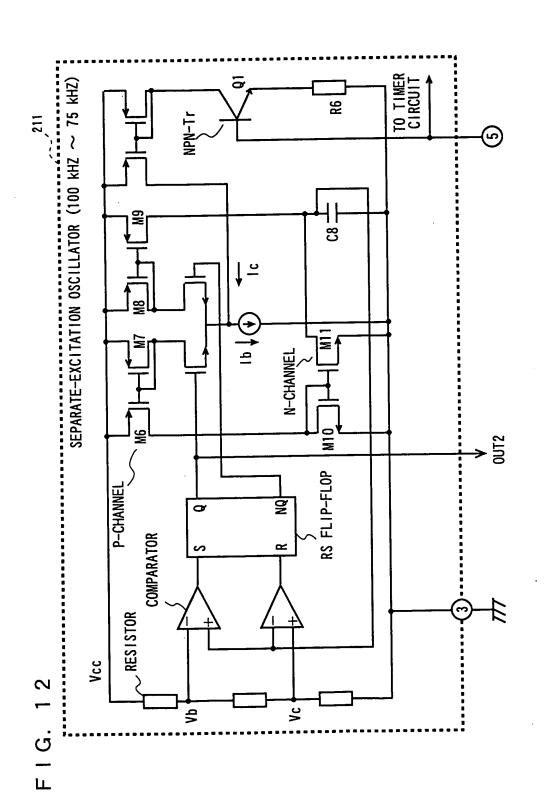
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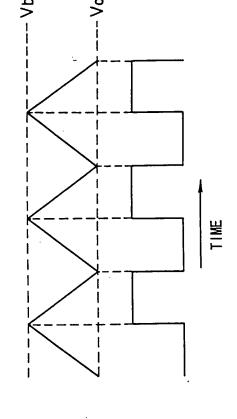
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FIG. 14

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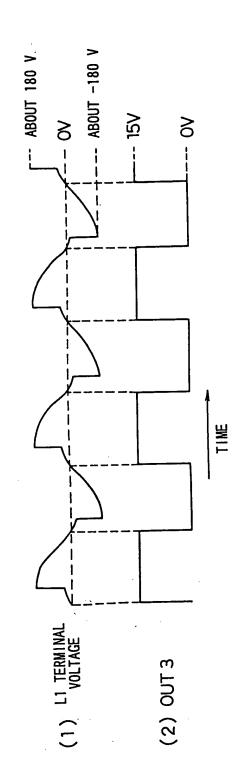
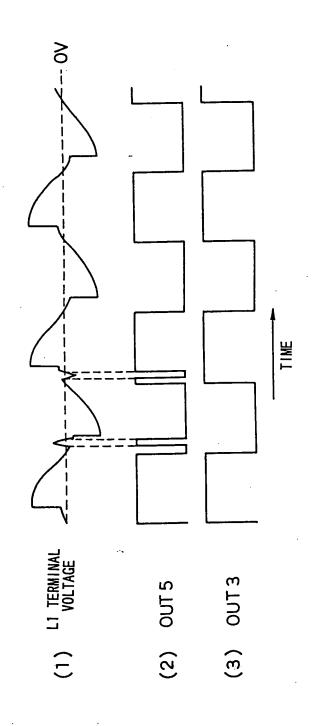
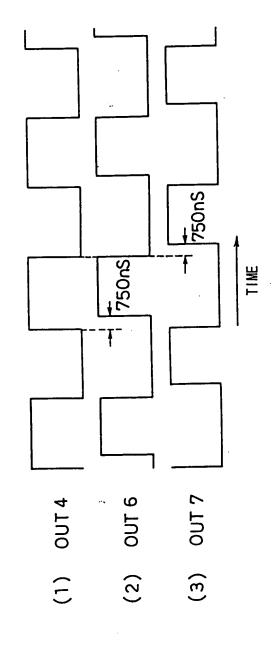
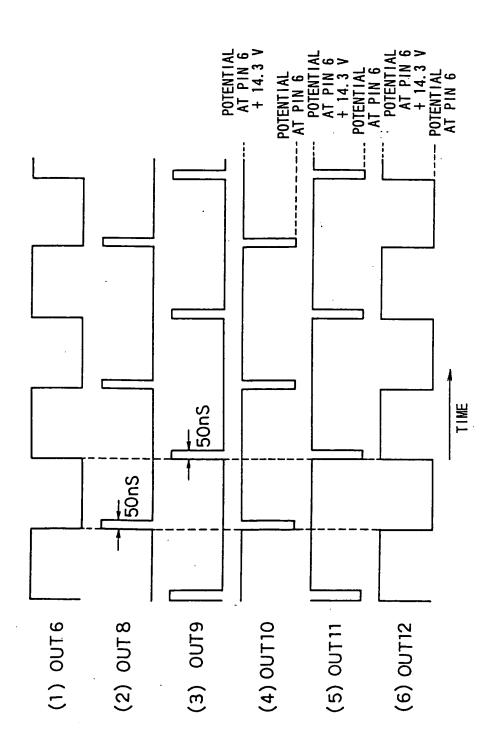


FIG. 16



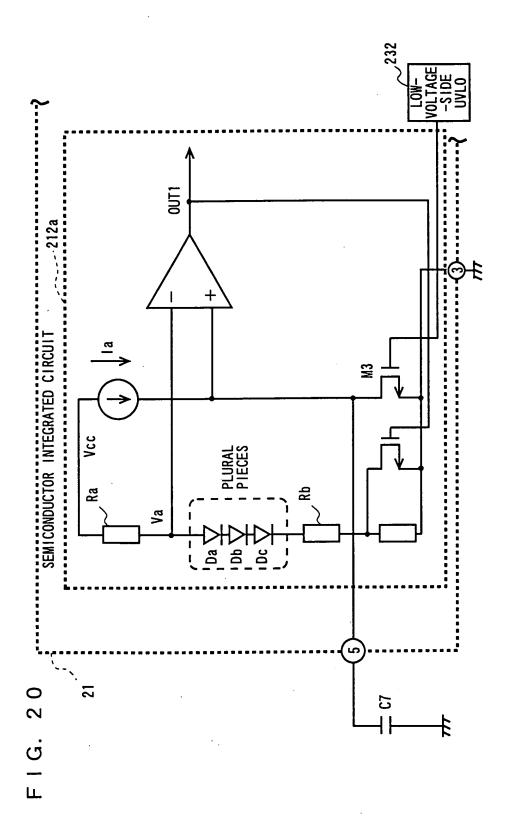
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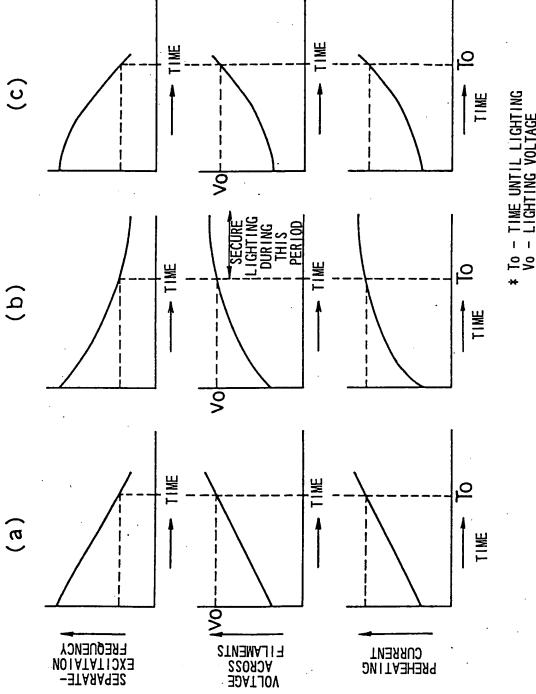
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FIG. 19



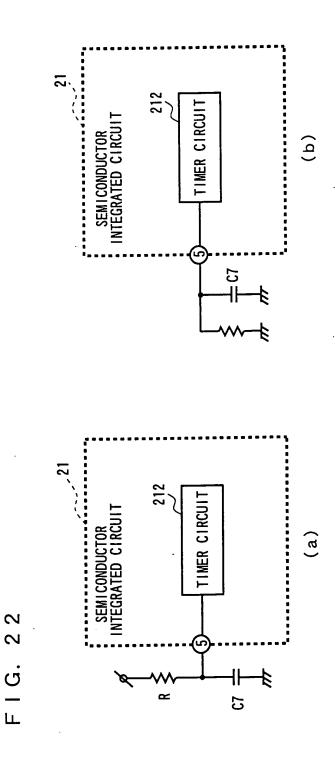
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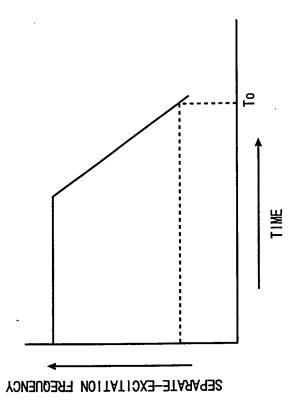
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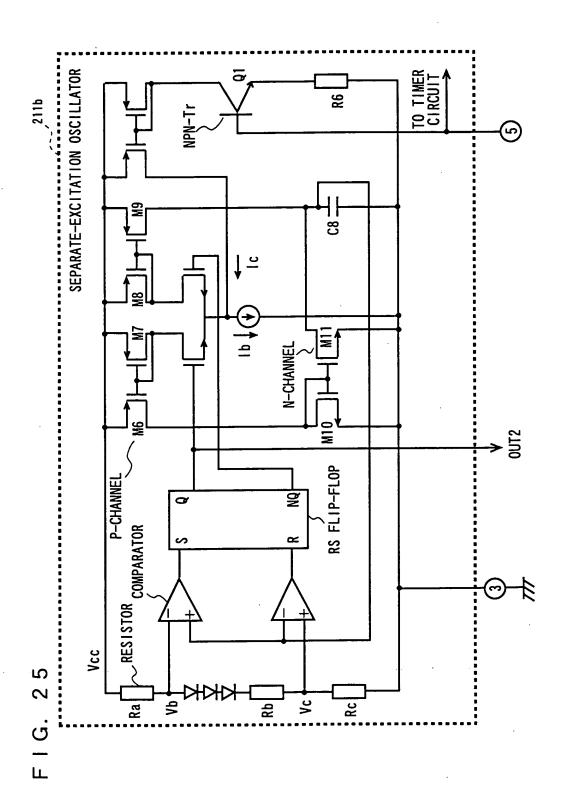
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FIG. 23

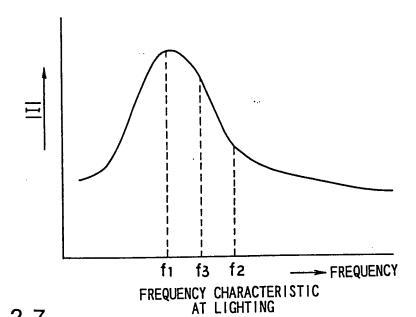


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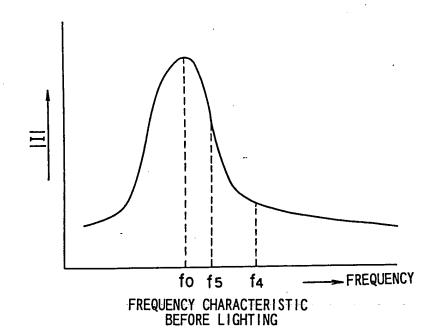


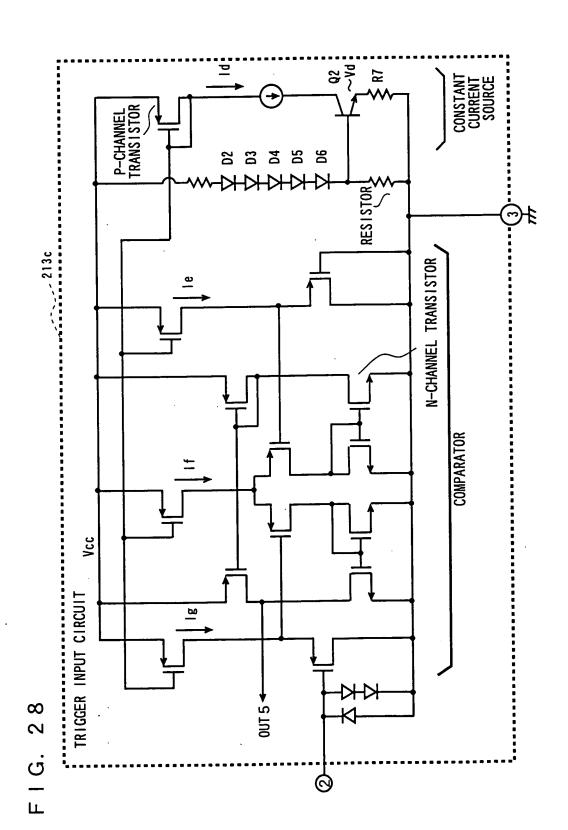
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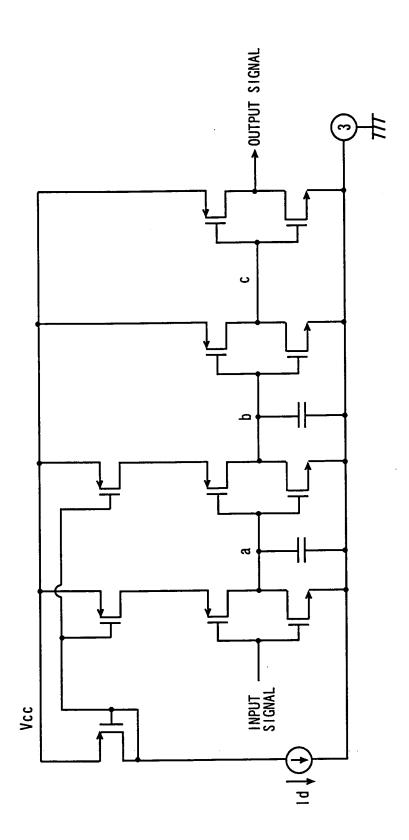
FIG. 27





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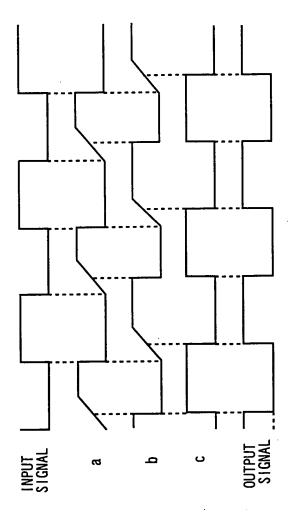
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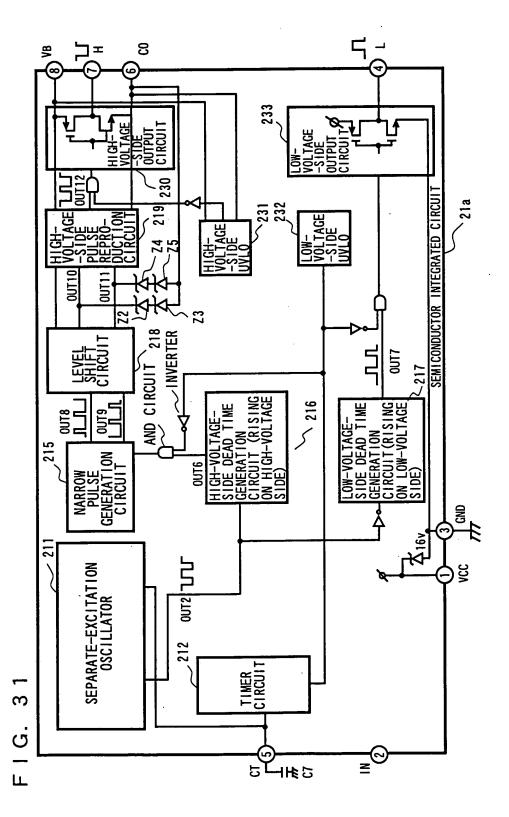
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FIG. 29



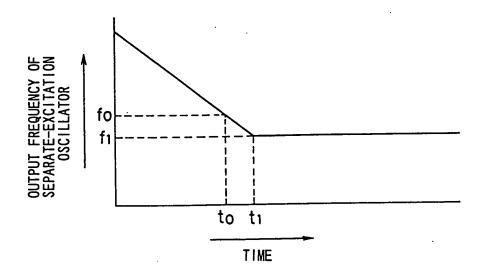
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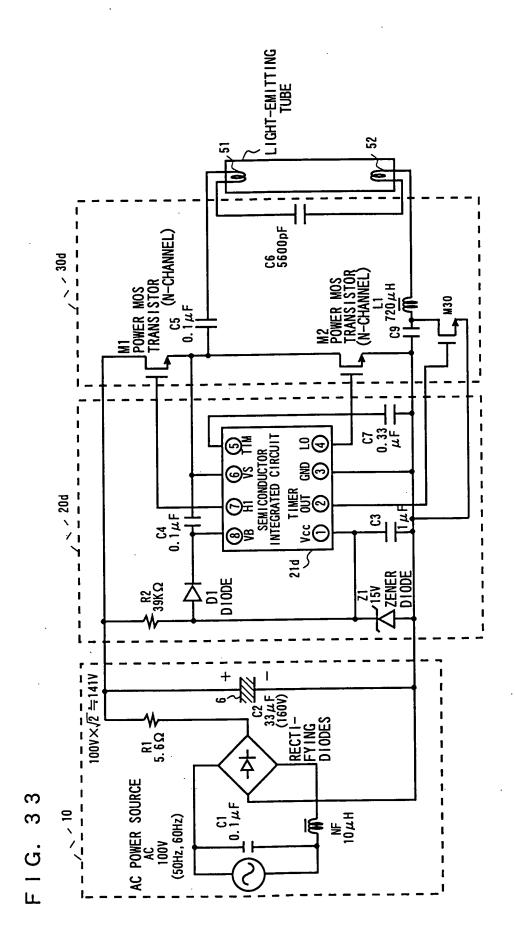


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FIG. 32





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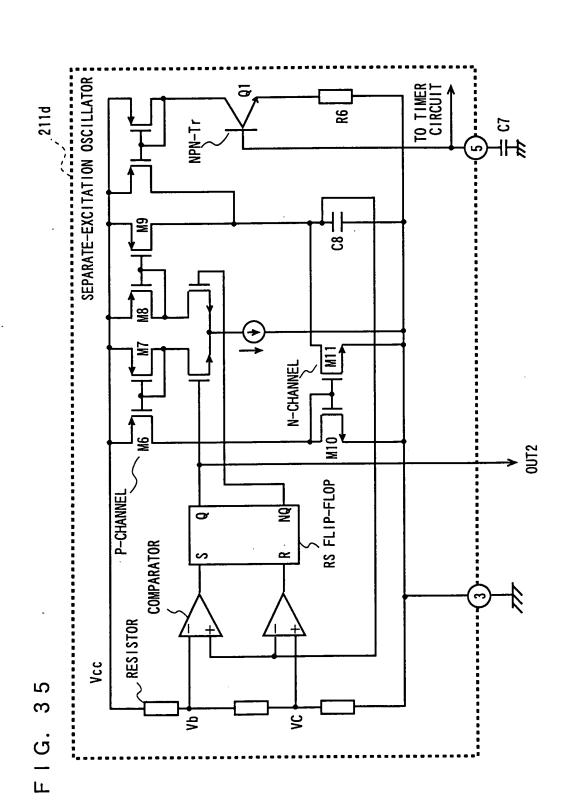
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**™**00 **₹) VBT** BLOCK DIAGRAM OF SEMICONDUCTOR INTEGRATED CIRCUIT 7232 LOW-VOLTAGE -SIDE UVLO HI-SIDE PULSE REPRO-DUCTION CIRCUIT VOLTAGE -SIDE UVLO 7231 DRS OUT IC LEVEL SHIFT 7117 0UT7 R40 HIGH-VOLTAGE-SIDE DEAD TIME GENERATION CIRCUIT (RISING ON HI SIDE) CIRCUIT (RISING) ON LO SIDE AND 216 **0UT8** 215 NARROW PULSE GENERATION CIRCUIT # GND 211d ည SEPARATE-EXCITATION 0SCILLATOR CIRCUIT <u>M</u> TIMER-OUT 212d 凰 Va Va Va Va 4 ന F ら ら  $\Box$ い針て

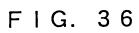
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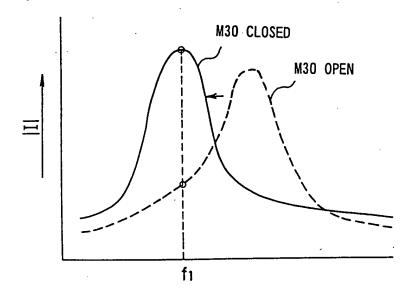
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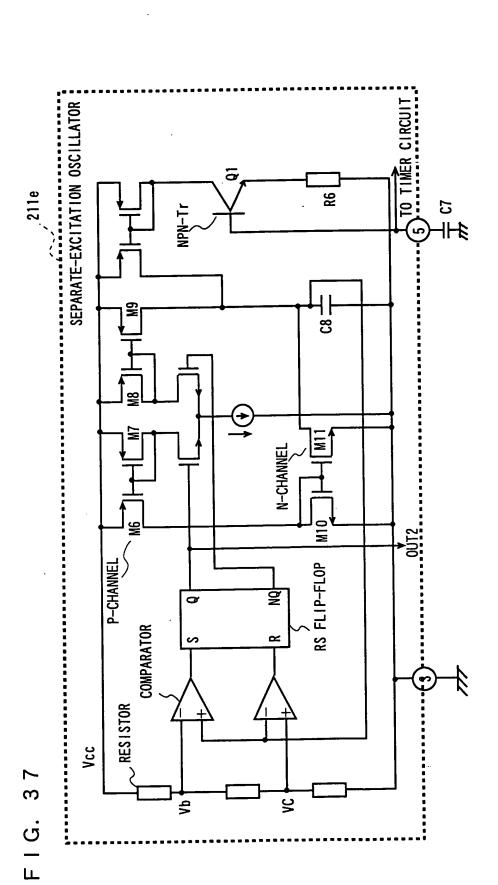
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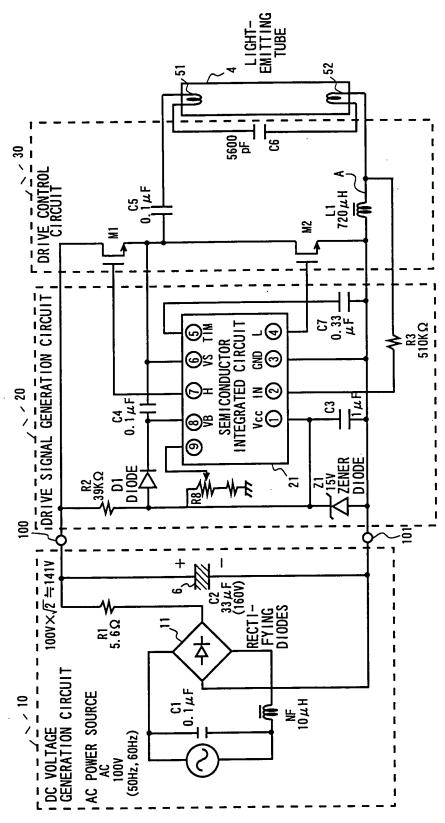




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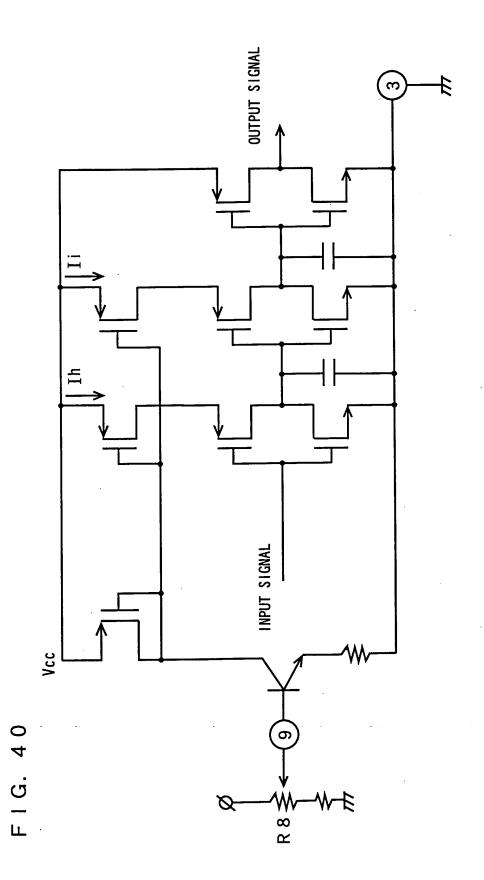


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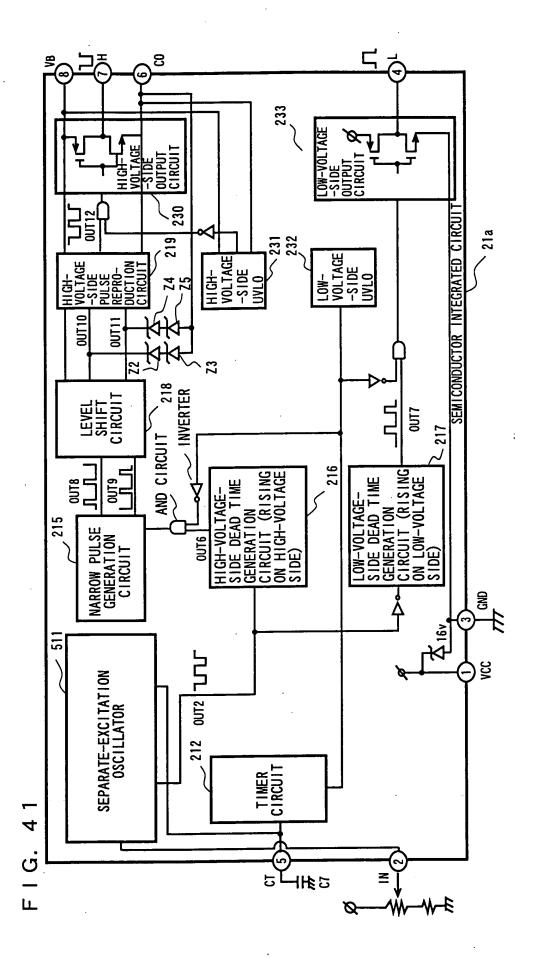
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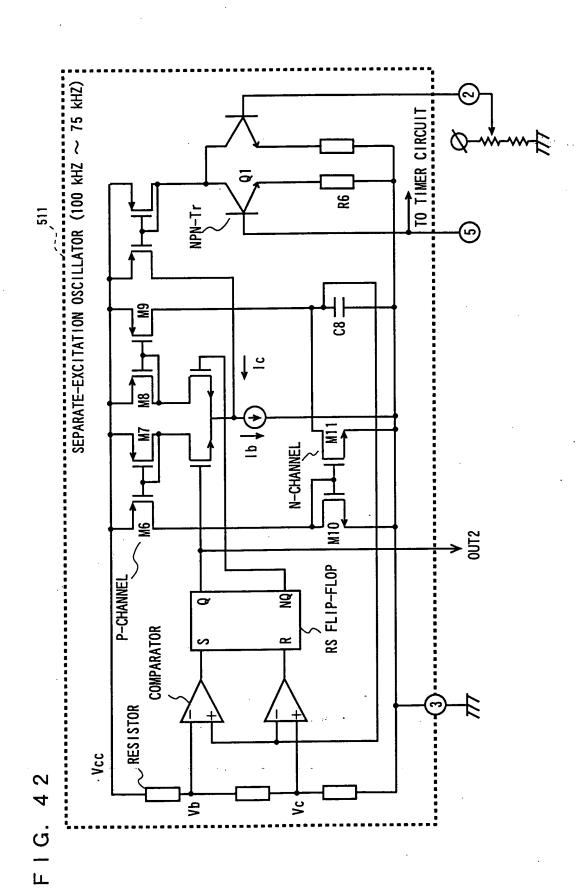
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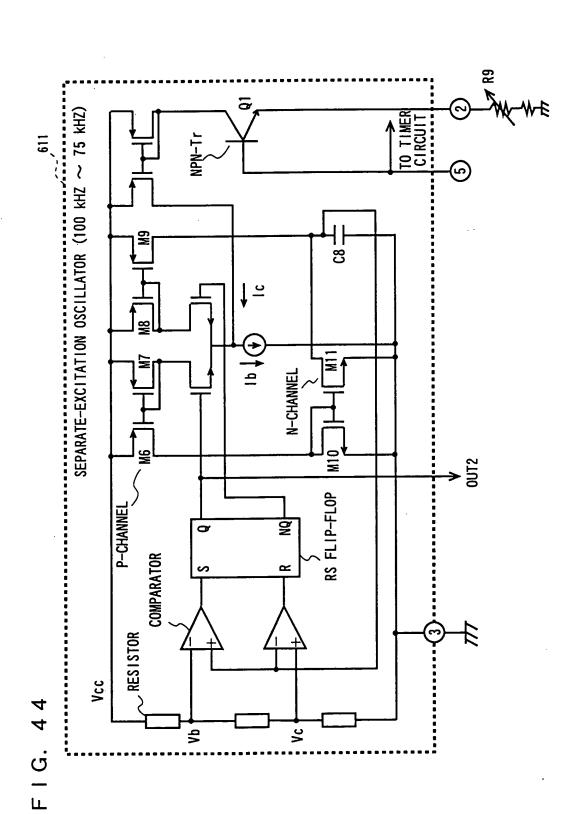
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8 LOW-VOLTAGE-SIDE OUTPUT 230 231 232 INTEGRATED CIRCU 21a 219 LOW-VOLTAGE -SIDE UVLO HIGH-VOLTAGE -SIDE UVLO: -SIDE PULSE REPRO-SEM I CONDUCTOR 218 INVERTER LEVEL SHIFT CIRCUIT 0UT7 7217 AND CIRCUIT HIGH-VOLTAGE-SIDE DEAD TIME GENERATION CIRCUIT (RISING ON HIGH-VOLTAGE SIDE) LOW-VOLTAGE-SIDE DEAD TIME GENERATION CIRCUIT (RISING ON LOW-VOLTAGE 7216 215 NARROW PULSE GENERATION CIRCUIT **0UT6** 8 <u>-</u> WIZ TIM ည SEPARATE-EXCITATION OSCILLATOR TIMER CIRCUIT က 4 Е С いまて S

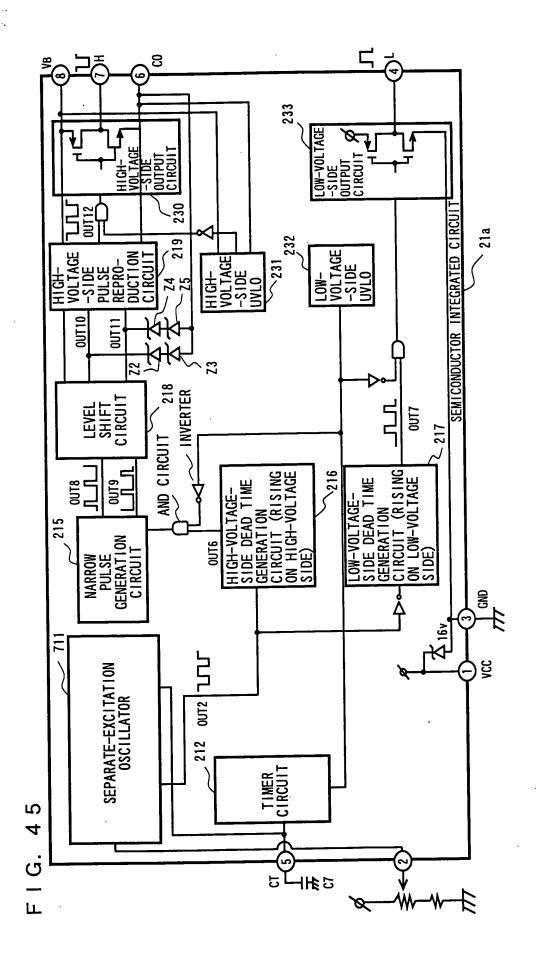
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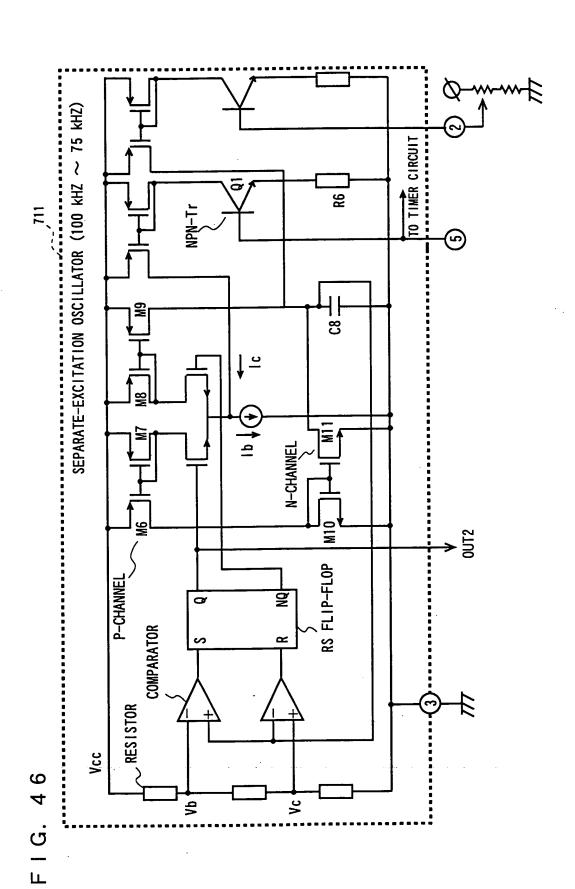
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